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PATENT NUMBER

U.S. UTILITY Patent Application

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PATENT-PAGE

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APPLICATION NO. 09/902672	CONT/PRIOR D F	CLASS 438	SUBCLASS 609	ART. UNIT (2825)	EXAMINER L. MILSTEIN
APPLICANTS Michimasa Furukawa					
TITLE Method of forming a semiconductor integrated circuit device					

PTO-2040
12/89

ISSUING CLASSIFICATION

 Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Assistant Examiner)	(Date)			ISSUE FEE
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					ISSUE BATCH NUMBER

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